

## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)    KOBAYASHI, THOMAS                      GROUP ART UNIT:    2891  
 APPLN. NO.:      10/075218                                      EXAMINER:    Dana Farahani  
 FILED:              2/14/2002  
 TITLE:              SEMICONDUCTOR DEVICE HAVING A FUSE AND METHOD OF  
                          FORMING THEREOF

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Certificate of Submission

I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.

\_\_\_ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.

\_\_\_ Facsimile transmitted in accordance with C.F.R. § 1.6(d).

  X   Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

June 18, 2007

\_\_\_\_\_  
Date of Submission

/Stacie Herrera/

\_\_\_\_\_  
Signature

Stacie Herrera

\_\_\_\_\_  
Printed Name of Person Signing Certificate

AMENDMENT

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated May 17, 2007, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.